BasicRX

Contents

The BasicRx/BasicTx daughterboards are low-cost daughterboards that provides direct access to the ADC inputs. The boards can accept real-mode signals from 1 to 250 MHz. The BasicRx/BasicTx is ideal for applications using an external front end providing relatively clean signals within operable bandwidth. Wideband transformers couple each RF input to a single channel of the USRP device's ADC. The signals sampled by the ADC are manipulated in the FPGA, and can be processed as two real-mode signals, or a single I-Q pair.

- 1-250 MHz coverage
- Real or Complex sampling

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Basic RX Schematics

Basic TX Schematics

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FPGA Resources

UHD Stable Binaries

UHD Source Code on Github