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The USRP Network Series offers high-bandwidth, high-dynamic range processing capability. The Gigabit Ethernet interface of the USRP Network Series allows high-speed streaming capability up to 50 MS/s in both directions (8-bit samples). These features, combined with plug-and-play MIMO capability make the USRP Network an ideal candidate for software defined radio systems with demanding performance requirements.

- Xilinx® Spartan® 3A-DSP 1800 FPGA
- 14 bit 100 MS/s dual ADC
- 16 bit 400 MS/s dual DAC
- Frequency range: DC - 6 GHz with suitable daughterboard
- Up to 50 MS/s in both directions
- Full duplex, SISO (1 Tx & 1 Rx)
- Fully-Coherent MIMO Capability
- Optional GPSDO
- Gigabit Ethernet connectivity

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- SBX-40
- UBX-40
- WBX-40
- CBX-40
- LFRX / LFTX
- BasicRX / BasicTX
- TVRX2
- DBSRX2

- SSB/LO Suppression -35/50 dBc
- Phase Noise 1.8 GHz 10kHz -80 dBc/Hz
- Phase Noise 1.8 GHz 100kHz -100 dBc/Hz
- Phase Noise 1.8 GHz 1MHz -137 dBc/Hz
- Power Output 15 dBm
- IIP3 (@ typ NF) 0 dBm
- Typical Noise Figure 5 dB

22 x 16 x 5 cm

- N200/N210 0-40 °C

- Dual Channel, 16-Bit DAC - [AD9777](#)
- Dual Channel, 14-Bit ADC - [ADS62P4X](#)
- FPGA - [XC3SD3400AFG676](#) - Double check
- AD56x3
- Clock Distribution IC - [AD9510](#)
- Gigabit Ethernet Transceiver - [ET1011C2](#)
- Pipelined SRAM - [CY7C1354C](#)
- Drivers/Receiver [MAX232](#)

1.2 kg

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- Gigabit Ethernet

[FPGA Resources](#)

[UHD Stable Binaries](#)

[UHD Source Code on Github](#)